	Application No.	Applicant(s)
Notice of Allowability	10/624,394 Examiner	STOCKSTAD, TROY L. Art Unit
•		
	Brian Young	2819
The MAILING DATE of this communication appear All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this ap or other appropriate communication GHTS. This application is subject to	plication. If not included n will be mailed in due course. THIS
1. \boxtimes This communication is responsive to <u>the RCE filed 6/1/04</u> .		
2. The allowed claim(s) is/are <u>1-27</u> .		
3. The drawings filed on 22 July 2003 are accepted by the Examiner.		
 4. ☐ Acknowledgment is made of a claim for foreign priority una a) ☐ All b) ☐ Some* c) ☐ None of the: 1. ☐ Certified copies of the priority documents have 2. ☐ Certified copies of the priority documents have 3. ☐ Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). * Certified copies not received: Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM 	been received. been received in Application No cuments have been received in this of this communication to file a reply	national stage application from the
 THIS THREE-MONTH PERIOD IS NOT EXTENDABLE. 5. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give 		
6. CORRECTED DRAWINGS (as "replacement sheets") mus	t be submitted.	
(a) including changes required by the Notice of Draftspers	on's Patent Drawing Review (PTO-	948) attached
1) 🗌 hereto or 2) 🔲 to Paper No./Mail Date		·
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date	s Amendment / Comment or in the C	Office action of
Identifying indicia such as the application number (see 37 CFR 1, each sheet. Replacement sheet(s) should be labeled as such in the		
7. DEPOSIT OF and/or INFORMATION about the deposit attached Examiner's comment regarding REQUIREMENT		
Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)		Patent Application (PTO-152) (PTO-413).
3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/0	Paper No./Mail Da	te ment/Comment
Paper No./Mail Date <u>6/1/04</u> 4. ☐ Examiner's Comment Regarding Requirement for Deposit		ent of Reasons for Allowance
of Biological Material		ETT OF REASONS TO Allowance

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1. Claims 1-27 are allowed.

2. The following is an examiner's statement of reasons for allowance: an analog-to-digital converter (ADC) wherein temperature-to- digital conversion is accomplished by first measuring a change in voltage Vbe across the junction of a diode when different current densities are forced through the junction and the obtained change is proportional to temperature wherein as pad of the conversion processing, Vbe is multiplied by a fixed gain, and an offset voltage value is subtracted from Vbe wherein the multiplication and subtraction functions are performed by a switched- capacitor integrator in a delta-sigma ADC, has not been shown in the prior art..

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Brown et al. disclose an A/D converter system and method with temperature compensation. The circuit includes a low noise analog-to-digital conversion chip and a precision voltage reference source. The voltage reference source includes a diode with two terminals and a passive attenuation circuit. The attenuation circuit and the diode are coupled in parallel between the two terminals to provide a voltage reference signal for use by the analog to digital conversion chip. The analog to digital chip uses the voltage reference signal to set the full-scale input range for the signal.

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Brian Young whose telephone number is 571-272-1816. The examiner can normally be reached on Mon-Fri 7:30-4:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mike Tokar can be reached on 571-272-1812. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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